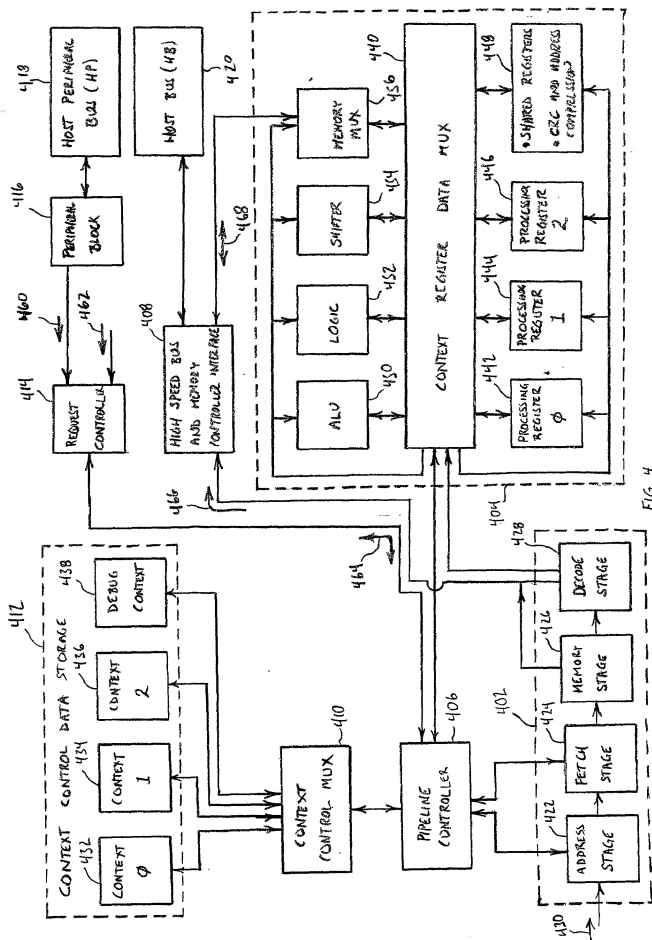


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Destination Address			Source Address		
Register	1W[23:20]	Register Name	Register	1W[19:16]	Register Name
R0	0000	GP Register 0	R0	0000	GP Register 0
R1	0001	GP Register 1	R1	0001	GP Register1
R2	0010	GP Register 2	R2	0010	GP Register 2
R3	0011	GP Register 3	R3	0011	GP Register 3
R4	0100	GP Register 4	R4	0100	GP Register 4
R5	0101	GP Register 5	R5	0101	GP Register 5
R6	0110	GP Register 6	R6	0110	GP Register 6
R7	0111	GP Register 7	R7	0111	GP Register 7
R8	1000	Reserved	R8	1000	Reserved
R9	1001	Reserved	R9	1001	Reserved
R10	1010	Pipel-Line	R10	1010	Pipel-Line
R11	1011	Stack Pointer	R11	1011	Stack Pointer
R12	1100	Command	R12	1100	Command
R13	1101	CRC Data/	R13	1101	CRC Data/
R14	1110	Addr Compression Data	R14	1110	Addr Compression Data
R15	1111	CRC Remainder Data	R15	1111	CRC Remainder Data
		CRC Polynomial			Immediate Data
		Addr Compression Mask			

FIG. 5



